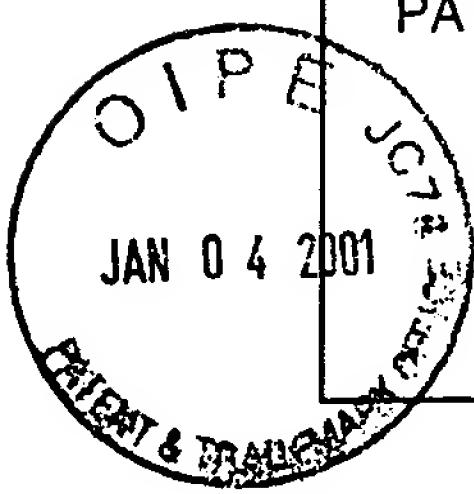


PTO-1449 (Modified)		ATTY. DOCKET NO. RA043D2DC	SERIAL NUMBER 09/545,648
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		APPLICANT(S) FARMWALD ET AL.	<b>RECEIVED</b>
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**U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
DA	4,633,735	05/05/87	Novak, et. al	—	—	
DA	5,684,753	11/04/97	Hashimoto, et al	—	—	
DA	4,322,635	03/30/81	Redwine	—	—	
DA	4,916,670	04/10/90	Suzuki et al.	—	—	
DA	5,006,982	04/09/91	Ebersole et al.	—	—	
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**FOREIGN PATENT DOCUMENTS**

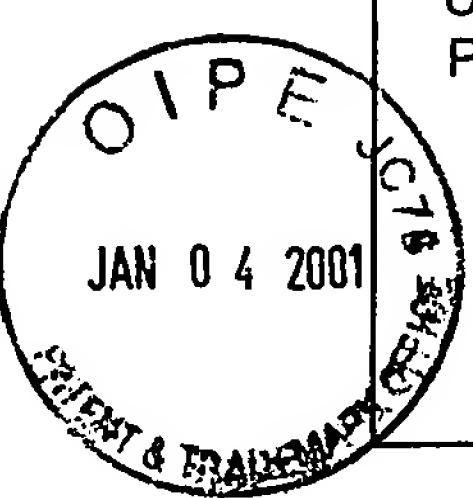
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

DA	Ikeda, Hiroaki et al., "100 MHz Serial Access Architecture for 4MB Field Memory," Symposium of VLSI Circuits, Digest of Technical Papers, pp. 11-12 (Jun. 1990)
DA	Takasugi, A. et al., "A DATA TRANSFER ARCHITECTURE FOR FAST MULTI-BIT SERIAL ACCESS MODE DRAM", 11TH European Solid State Circuits Conference, Toulouse France pp. 161-165 (Sep. 1985)
DA	Ray Pinkham et al., "A 128Kx8 70-MHz Multiport Video RAM with Auto Register Reload and 8x4 WRITE Feature," IEEE Journal of Solid State Circuits, vol. 23, no. 3, pp. 1133-1139 (Oct. 1988)
DA	Graham, Andy et al., "Pipelined static RAM endows cache memories with 1-ns speed", Electronic Design pp. 157-170 (Dec. 1984)
DA	Robert J. Lodi et al., "Chip and System Characteristics of a 2048-Bit MNOS-BORAM LSI Circuit," IEEE International Solid-State Circuits Conference, (Feb. 1976)
DA	Pinkham, Raymond, "A High Speed Dual Port Memory with Simultaneous Serial and Random Mode Access for Video Applications," IEEE Journal of Solid-State Circuits, Vol. SC-19, No. 6, pp. 999-1007 (Dec. 1984)
DA	Ishimoto, S. et al., "A 256K Dual Port Memory," ISSCC Digest of Technical Papers, p. 38-39 (Feb. 1985)

EXAMINER	Glenne Anne	DATE CONSIDERED	3/22/2001
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PTO-1449 (Modified)		ATTY. DOCKET NO. RA043D2DC	SERIAL NUMBER 09/545,648
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AA	4,979,145	12/18/90	Remington et al.	—	—	
AA	5,276,846	01/04/94	Aichelmann Jr., et. al	—	—	

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AA	Tomoji Takada et al., "A Video Codec LSI for High-Definition TV Systems with One-Transistor DRAM Line Memories," IEEE Journal of Solid-State Circuits, Vol. 24, No. 6, pp. 1656-1659 (Dec. 1989)
AA	Amitai, Z., "Burst Mode Memories Improve Cache Design," WESCON/90 Conference Record, pp. 29-32 (Nov. 1990)
AA	Robert J. Lodi et al., "MNOS-BORAM Memory Characteristics," IEEE Journal of Solid-State Circuits, vol. SC-11, No. 5, pp. 622-631 (Oct. 1976)
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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
GA	4,482,999	11/13/84	Janson et al.	—	—	
JA	5,029,124	07/02/91	Leahy et al.	—	—	
JA	5,193,193	03/09/93	Iyer	—	—	
JA	4,926,385	05/15/90	Fujishima et al.	—	—	
JA	4,566,099	01/21/86	Magerl	—	—	
JA	4,803,621	02/07/89	Kelly	—	—	
JA	4,589,108	05/13/86	Billy	—	—	
JA	4,870,622	09/26/89	Aria et al.	—	—	
JA	4,878,166	10/31/89	Johnson et al.	—	—	
JA	4,849,965	07/18/89	Chomel et al.	—	—	
JA	4,851,990	07/25/89	Johnson et al.	—	—	

## FOREIGN PATENT DOCUMENTS

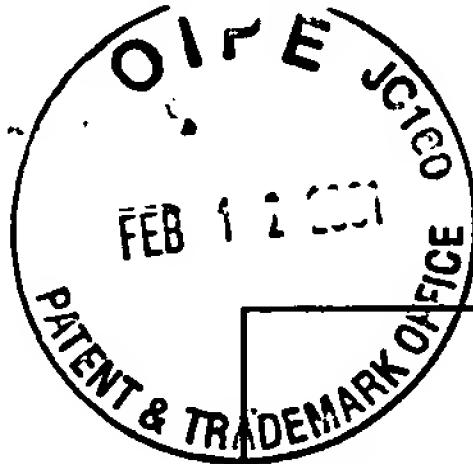
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


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PTO-1449 (Modified)		ATTY. DOCKET NO. RA001C11	SERIAL NUMBER 09/669,295
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		APPLICANT(S) FARMWALD ET AL.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE September 25, 2000	GROUP ART UNIT 2181

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## U.S. PATENT DOCUMENTS

<u>EXAMINER INITIALS</u>	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
JA	4,528,661	07/09/85	Bahr et al.	—	—	
JA	4,048,673	09/13/77	Hendrie et al.	—	—	
JA	4,748,617	05/31/88	Drewlo	—	—	
JA	4,839,801	06/13/89	Nicely et al.	—	—	
JA	4,949,301	08/14/90	Joshi et al.	—	—	
JA	3,950,735	04/13/76	Patel	—	—	
JA	4,047,246	09/06/77	Kerllenevich et al.	—	—	
JA	4,763,249	08/09/88	Bomba et al.	—	—	
JA	4,625,307	11/25/86	Tulpule et al.	—	—	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


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